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APPLICATION NO. FILING DATE		ING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/648,022 08/26/2003		Christopher N. Brindle	17987	3501		
26794	7590	02/28/2006		EXAMINER		
		CS CORPORA	WELLS, KI	WELLS, KENNETH B		
WILMINGT		ILL ROAD, SU 19808	ART UNIT	PAPER NUMBER		
				2816		
				DATE MAILED: 02/28/2006		

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Applicatio	n No.	Applicant(s)				
		10/648,02	22 BRINDLE, CHRISTOPHER N		OPHER N.			
	Office Action Summary	Examiner		Art Unit				
		Kenneth B.		2816				
Period fo	The MAILING DATE of this communication or Reply	n appears on the	cover sheet with the c	orrespondence add	ress			
A SHOWHIC - Exter after - If NO - Failu Any r	ORTENED STATUTORY PERIOD FOR RECHEVER IS LONGER, FROM THE MAILING IS IN A STATE OF THE MAILING IS IN A	NG DATE OF TH CFR 1.136(a). In no eve on. period will apply and will statute, cause the appli	IS COMMUNICATION int, however, may a reply be time expire SIX (6) MONTHS from cation to become ABANDONEI	l. ely filed the mailing date of this com D (35 U.S.C. § 133).				
Status								
1)	Responsive to communication(s) filed on	16 September 2	005					
•	•	This action is no						
'	Since this application is in condition for al	=		secution as to the r	merits is			
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Dianasiti	on of Claims	aur Expans qui	.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,					
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	Claim(s) <u>1,2,6-11,13-15,18 and 19</u> is/are							
	4a) Of the above claim(s) is/are wit	indrawn from cor	isideration.					
·	5) Claim(s) is/are allowed.							
•	Claim(s) <u>1, 2, 6-11, 13-15, 18 and 19</u> is/a	re rejected.						
	Claim(s) is/are objected to.	and/or alastian m	. au iromont					
ا(٥	Claim(s) are subject to restriction a	and/or election re	equirement.					
Applicati	on Papers							
9)[The specification is objected to by the Exa	aminer.	•					
10)	The drawing(s) filed on is/are: a)[•						
	Applicant may not request that any objection							
	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.								
Priority (ınder 35 U.S.C. § 119							
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: Certified copies of the priority documents have been received. Certified copies of the priority documents have been received in Application No Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 								
2) Notice 3) Information	t(s) be of References Cited (PTO-892) be of Draftsperson's Patent Drawing Review (PTO-94 mation Disclosure Statement(s) (PTO-1449 or PTO/94 or No(s)/Mail Date		4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	ite	·152)			

1. As indicated during the telephonic interview conducted on 02/14/06 with applicant's representative, the following office action replaces the final rejection mailed on 12/29/05 (which inadvertently omitted claims 1 and 2 from the prior art rejection). The period for statutory periof for response to the previous office action is hereby reset, and now runs three (3) months from the mailing date of this new office action.

- 2. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
- 3. Claims 1, 2, 6-11, 13-15, 18 and 19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sasabata et al in view of Kawai.

Note figure 5 of Sasabata et al, where the FET is transistor 43, the first port is terminal 48, the second port is terminal 49, the first control signal input is the voltage applied to the gate of transistor 43 and the second control signal input is the signal applied to terminal 48. The shunt FET, though not disclosed, would have been obvious to one of ordinary skill in the art because it is

well known in the art that a switch such as that included within circuit 52 is typically formed using a transmission gate structure such as that disclosed in figures 7 and 8 of Kawai. The motivation for using such a structure as that disclosed by Kawai is simply to obtain the well-known advantages associated with such a transmission gate structure (and also to provide a specific implementation of the switch in figure 5 of Sasabata et al which is shown generically). Once this specific implementation is made, there will be an FET shunting the gate of transistor 43 to ground (corresponding to applicants shunt transistor 46 in instant figure 3). This shunt FET in the combined Sasabata et al/Kawai circuitry will have a drain terminal directly coupled to the gate terminal of Sasabata et al's transistor 43.

4. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, THIS ACTION IS MADE FINAL. See MPEP § 706.07(a).

Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date

of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kenneth B. Wells whose telephone number is (571)272-1757. The examiner can normally be reached on Monday through Friday from 8:30am to 5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P.

Callahan, can be reached at (571)272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information

Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Kenneth B. Wells Primary Examiner Art Unit 2816

February 17, 2006